

IN THE SPECIFICATION

Please amend paragraph [0023] of the specification as indicated below:

[0023] Figure 4a illustrates another balun according to one embodiment of the present invention, which has the capability to transform a differential signal to a single ended signal and vice-versa. Transformer 400 includes inductors 408, 410 and 414. As shown in Figures 4b1, the inductors 408 and 410 are interleaved to provide good magnetic coupling between them. According to one embodiment, they are disposed in the same layer except at areas 420a-1, and 420a-2 where one inductor crosses over the other. In one embodiment illustrated in Figures 4b2a-b, inductor 408 is entirely disposed on metal layer 408', with inductor 410 crossing inductor 408 using vias 422 that electrically connect to another metal layer 410' above or below the metal layer ~~408'~~ 408' to facilitate the cross over. In the embodiment shown in Figures 4b2a-b, inductor 408 makes two right angle turns in metal layer 408' at the cross over area 420a-1 and inductor 410 makes two right angle turns in metal layer 410' between vias 422, and although Although not shown in Figures 4b2a-b, but in a similar manner, inductors 408 and 410 also makes each make two right angle turns at the cross over area 420a-2. In the embodiment shown in Figure 4b3, inductor 410 crosses over inductor 408 at an angle, such as, for example, 45 degrees or some other angle. Further, as shown in Figure 4c inductor 414 is stacked on a different metal layer 414', disposed adjacent to one of layers 408' and 410', and allows inductor 414 to couple to each of inductors 408 and 410.

Please amend the Abstract as indicated below:

A ~~method for producing~~ an on-chip signal transforming device. ~~The method~~ includes ~~providing~~ a substrate, and ~~laying~~ a first conductive layer above the substrate, wherein the first conductive layer has a plurality of interleaved inductors. ~~The method~~ the device further includes ~~laying~~ a second conductive layer above the substrate, wherein the second conductive layer has at least one inductor.